**/Project/ChangeCounter**

**/**

**TSP(SM) Rollup Plan Summary**

**Quality Summary**

**Defect Density**

|  |  |  |
| --- | --- | --- |
| **Defects/Page** | **Plan** | **Actual** |
| Requirements Review | #DIV/0! | #DIV/0! |
| Requirements Inspection | #DIV/0! | #DIV/0! |
| HLD Review | #DIV/0! | #DIV/0! |
| HLD Inspection | #DIV/0! | #DIV/0! |

|  |  |  |
| --- | --- | --- |
| **Defects/KLOC** | **Plan** | **Actual** |
| Detailed Design Review | #DIV/0! | #DIV/0! |
| Detailed Design Inspection | #DIV/0! | #DIV/0! |
| Code Review | #DIV/0! | #DIV/0! |
| Compile | #DIV/0! | #DIV/0! |
| Code Inspection | #DIV/0! | #DIV/0! |
| Unit Test | #DIV/0! | #DIV/0! |
| Build and Integration Test | #DIV/0! | #DIV/0! |
| System Test | #DIV/0! | #DIV/0! |
| Acceptance Test | #DIV/0! | #DIV/0! |
| Product Life | #DIV/0! | #DIV/0! |
| Total Development | #DIV/0! | #DIV/0! |
| Total | #DIV/0! | #DIV/0! |

**Phase Ratios**

|  |  |  |
| --- | --- | --- |
| **Defect Ratios** | **Plan** | **Actual** |
| Detailed Design Review/Unit Test | 192 | #DIV/0! |
| Code Review/Compile | 4.67 | #DIV/0! |

|  |  |  |
| --- | --- | --- |
| **Development Time Ratios** | **Plan** | **Actual** |
| Text Pages (Appraisal/Development) | 0 | 0 |
| Requirements Pages (Appraisal/Development) | #DIV/0! | #DIV/0! |
| HLD Pages (Appraisal/Development) | 0.14 | 0 |
| Detailed Design/Code | #DIV/0! | #DIV/0! |
| DLD Review/Detailed Design | 0.21 | 0.15 |
| Code Review/Code | #DIV/0! | #DIV/0! |

**Inspection / Review Rates**

|  |  |  |
| --- | --- | --- |
|  | | |
|  | **Plan** | **Actual** |
| Requirements Review | 0 | 0 |
| Requirements Inspection | 0 | 0 |
| HLD Review | #DIV/0! | #DIV/0! |
| HLD Inspection | 0 | #DIV/0! |
| Detailed Design Review | #VALUE! | #VALUE! |
| Detailed Design Inspection | #VALUE! | #VALUE! |
| Code Review | #DIV/0! | #DIV/0! |
| Code Inspection | #DIV/0! | #DIV/0! |

**Cost of Quality**

|  |  |  |
| --- | --- | --- |
|  | **Plan** | **Actual** |
| % Appraisal COQ | 16.6% | 18.8% |
| % Failure COQ | 0% | 0% |
| AFR | #DIV/0! | #DIV/0! |

**Phase Yields**

|  |  |  |
| --- | --- | --- |
|  | | |
|  | **Plan** | **Actual** |
| Management and Miscellaneous | 0% | #DIV/0! |
| Launch and Strategy | 0% | #DIV/0! |
| Planning | 0% | 0% |
| Requirements | 0% | 0% |
| System Test Plan | 0% | 0% |
| Requirements Review | 70% | 0% |
| Requirements Inspection | 70% | 0% |
| High-Level Design | 0% | 0% |
| Integration Test Plan | 0% | 0% |
| HLD Review | 70% | 0% |
| HLD Inspection | 70% | 0% |
| Detailed Design | 0% | 100% |
| Detailed Design Review | 70% | #DIV/0! |
| Test Development | 0% | #DIV/0! |
| Detailed Design Inspection | 70% | #DIV/0! |
| Code | 0% | #DIV/0! |
| Code Review | 70% | #DIV/0! |
| Code Inspection | 70% | #DIV/0! |
| Unit Test | 90% | #DIV/0! |
| Build and Integration Test | 80% | #DIV/0! |
| System Test | 80% | #DIV/0! |
| Documentation | 0% | #DIV/0! |
| Acceptance Test | 0% | #DIV/0! |
| Postmortem | 0% | #DIV/0! |
| Product Life | 0% | #DIV/0! |

**Process Yields**

|  |  |  |
| --- | --- | --- |
| **% Removed Before:** | | |
|  | **Plan** | **Actual** |
| Unit Test | 99.7% | 100% |
| Build and Integration Test | 100.0% | 100% |
| System Test | 100.0% | 100% |
| Acceptance Test | 100.0% | 100% |
| Product Life | 100.0% | 100% |

**Defect Injection Rates**

|  |  |  |
| --- | --- | --- |
| **Defects Injected per Hour** | | |
|  | **Plan** | **Actual** |
| Management and Miscellaneous | 0 | 0 |
| Launch and Strategy | 0 | 0 |
| Planning | 0 | 0.1 |
| Requirements | 0.25 | #DIV/0! |
| System Test Plan | 0 | 0 |
| Requirements Review | 0 | 0 |
| Requirements Inspection | 0 | 0 |
| High-Level Design | 0.25 | 0 |
| Integration Test Plan | 0 | 0 |
| HLD Review | 0 | #DIV/0! |
| HLD Inspection | 0 | #DIV/0! |
| Detailed Design | 0.75 | 0 |
| Detailed Design Review | 0 | 0 |
| Test Development | 0 | 0 |
| Detailed Design Inspection | 0 | #DIV/0! |
| Code | 2 | #DIV/0! |
| Code Review | 0 | #DIV/0! |
| Code Inspection | 0 | #DIV/0! |
| Unit Test | 0.07 | #DIV/0! |
| Build and Integration Test | 0 | #DIV/0! |
| System Test | 0 | #DIV/0! |
| Documentation | 0 | 0 |
| Acceptance Test | 0 | #DIV/0! |
| Postmortem | 0 | 0 |
| Product Life | 0 | #DIV/0! |

**Defect Removal Rates**

|  |  |  |
| --- | --- | --- |
| **Defects Removed per Hour** | | |
|  | **Plan** | **Actual** |
| Management and Miscellaneous | 0 | 0 |
| Launch and Strategy | 0 | 0 |
| Planning | 0 | 0 |
| Requirements | #DIV/0! | #DIV/0! |
| System Test Plan | 0 | 0 |
| Requirements Review | 0 | 0 |
| Requirements Inspection | 0 | 0 |
| High-Level Design | 0 | 0 |
| Integration Test Plan | 0 | 0 |
| HLD Review | #DIV/0! | #DIV/0! |
| HLD Inspection | 0.38 | #DIV/0! |
| Detailed Design | 0 | 0.18 |
| Detailed Design Review | 2.57 | 0 |
| Test Development | 0 | 0 |
| Detailed Design Inspection | 0.96 | #DIV/0! |
| Code | #DIV/0! | #DIV/0! |
| Code Review | #DIV/0! | #DIV/0! |
| Code Inspection | #DIV/0! | #DIV/0! |
| Unit Test | #DIV/0! | #DIV/0! |
| Build and Integration Test | #DIV/0! | #DIV/0! |
| System Test | #DIV/0! | #DIV/0! |
| Documentation | 0 | 0 |
| Acceptance Test | #DIV/0! | #DIV/0! |
| Postmortem | 0 | 0 |
| Product Life | #DIV/0! | #DIV/0! |

*TSP(SM) is a service mark of Carnegie Mellon University.*

*Report generated at 8:51 AM on Apr 2, 2014*